



10/800749

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of: **Takashi SAIKI et al.**

Patent No.: **7,321,151**

Issued: **January 22, 2008**

For: **SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING THE SAME**

Attorney Docket Number: **042193**

Customer Number: **38834**

**REQUEST FOR CERTIFICATE OF CORRECTION**

**Mail Stop: COC**

May 27, 2009

Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

Sir:

The undersigned requests that a Certificate of Correction be issued for the above-identified patent as indicated on the attached Form PTO-1050.

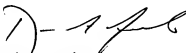
This request is being made in order to correct Figure 1B by replacing ref. #4 with ref. #7. It is respectfully submitted that no new matter has been added.

Because this error is a Patent and Trademark Office printing error, it is respectfully submitted that no fee is required.

If any fees are required in connection with this paper, please charge Deposit Account No. 50-2866.

Respectfully submitted,

**WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP**

  
Darrin A. Auito  
Attorney for Applicants  
Registration No. 56,024  
Telephone: (202) 822-1100  
Facsimile: (202) 822-1111

**Certificate**  
**MAY 29 2009**  
**of Correction**

DAA/tds

Enclosures: Form PTO-1050

# UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO.: 7,321,151

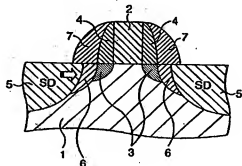
DATED: January 22, 2008

INVENTOR(S): Takashi SAIKI et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

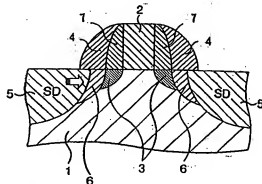
In Fig. 1B  
Change

FIG. 1B



To be

FIG. 1B



MAILING ADDRESS OF SENDER:  
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PATENT NO. 7,321,151

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